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Rabala

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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: : Pervez H. Sagarwala et al
Prior Serial No. : 08/885,636
Filed : Herewith
For : CMOS INTEGRATED CIRCUIT DEVICE WITH LDD
N-CHANNEL TRANSISTOR AND NON-LDD P-
CHANNEL TRANSISTOR
Prior Group No. : 2822
Prior Examiner : M. Trinh

BOX PATENT APPLICATION
Assistant Commissioner for Patents
Washington, D. C. 20231

Sir:

<p align="center">CERTIFICATE OF MAILING</p> <p>Express Mail Label No.: <u>EL 749593348 US</u></p> <p>I certify that this correspondence is being deposited with the United States Postal Service as "Express Mail, Post Office to Addressee" in an envelope with sufficient postage addressed to: BOX PATENT APPLICATION, Assistant Commissioner for Patents, Washington, D.C. 20231, on March 6, 2001.</p> <p><u>KATHY LONGENECKER</u> (Printed or typed name of person signing the certificate)</p> <p><u>Kathy Longenecker</u> (Signature of the person signing the certificate)</p>

PRELIMINARY AMENDMENT

Prior to examination, please amend the above-identified divisional application as follows:

IN THE SPECIFICATION

1. Please insert the following statement on page 1 of the specification:

A1
"This application is a division of prior U.S. Application Serial No. 08/885,636 filed on June 30, 1997,"
and now US Patent 6,221,709
X

MP